

## AMENDMENTS TO THE CLAIMS

Please replace the claims with the following listing of claims:

1. (Currently Amended) An apparatus for updating a code image, comprising:  
a loader configured to load a new code image into a temporary memory location separate from a memory space occupied by and used by an old code image;  
a logic module configured to identify incompatibilities between the old code image and the new code image from version information, a difference in initialization requirements, and a difference in size and location between the old code image and the new code image;  
a bootstrap module, within the new code image, configured to reconcile the incompatibilities by changing an initialization order; and  
a copy module configured to copy the new code image into the memory space occupied by the old code image.
2. (Original) The apparatus of claim 1, wherein the old code image is updated substantially concurrent with normal execution of transactions by the apparatus.
3. (Original) The apparatus of claim 1, further comprising an initialization module configured to initiate execution of a run-time segment of the new code image.
4. (Previously Presented) The apparatus of claim 1, wherein the copy module copies the new code image into the memory space after the bootstrap module reconciles the incompatibilities.

5. (Currently Amended) The apparatus of claim 1, wherein the logic module accesses the version information for the old code image and version information for the new code image to identify an incompatibility based at least in part on a difference between the version information.

6. (Previously Presented) The apparatus of claim 5, wherein the bootstrap module is configured to reconcile the incompatibility by updating modules that interface with the new code image.

7. (Previously Presented) The apparatus of claim 1, wherein the bootstrap module is configured to reconcile incompatibilities by associating persistent data of the old code image with the new code image, such that the persistent data is available in response to execution of the run-time segment of the new code image.

8. (Canceled)

9. (Original) The apparatus of claim 1, wherein at least one of the incompatibilities comprises a difference between data structures used by the old code image and data structures used by the new code image.

10. (Currently Amended) An apparatus for updating a code image, comprising:  
an update module configured to load a new code image into a temporary memory location separate from a memory space occupied by and used by an old code image;  
a logic module configured to identify incompatibilities between the old code image and the new code image from version information, a difference in initialization requirements, and a difference in size and location between the old code image and the new code image; and  
a bootstrap module within the new code image that executes subsequent to the update module, the bootstrap module configured to reconcile the incompatibilities by changing an initialization order prior to copying the new code image into the memory space occupied by the old code image.

11. (Currently Amended) The apparatus of claim 10, wherein the bootstrap module is configured to reconcile incompatibilities based on the version information for the old code image and the new code image, and wherein a copy module is configured to copy the new code image over the old code image in after the incompatibilities have been reconciled.

12. (Original) The apparatus of claim 10, wherein at least one of the incompatibilities comprises a difference between data structures used by the old code image and data structures used by the new code image.

13. (Currently Amended) A system that overlays an old code image with a new code image with minimal interruption of operations being performed by execution of the old code image, the system comprising:

a memory comprising an old code image and a buffer configured to store a new code image;

a processor executing instructions of the old code image to perform one or more operations, the processor configured to execute instructions of the old code image and the new code image;

a data structure configured to store an old code image pointer and a new code image pointer;

wherein, in response to an interrupt, the processor begins identifying incompatibilities between the old code image and the new code image from version information, a difference in initialization requirements, and a difference in size and location between the old code image and the new code image and executing bootstrap code within the new code image, the bootstrap code configured to reconcile incompatibilities between the old code image and the new code image by changing an initialization order.

14. (Original) The system of claim 13, wherein the bootstrap code overlays the new code image in memory with the old code image in response to reconciliation of the incompatibilities.

15. (Original) The system of claim 14, wherein in response to the interrupt, the processor executes an update module of the old code image that loads the new code image into the buffer.

16. (Original) The system of claim 15, wherein the update module stores the old code image pointer and the new code image pointer in the data structure.

17. (Original) The system of claim 16, wherein the update module reads a new code image header identified by the new code image pointer to determine the location of the bootstrap code within the new code image.

18. (Currently Amended) The system of claim 17, wherein the bootstrap code further reconciles the incompatibilities by updating modules that interface with the new code image.

19. (Currently Amended) The system of claim 18, wherein the bootstrap code further reconciles the incompatibilities by associating persistent data of the old code image with the new code image.

20. (Currently Amended) A method for updating a code image, comprising:  
loading a new code image into a temporary memory location separate from a memory space occupied by and used by an old code image;

identifying incompatibilities between the old code image and the new code image from version information, a difference in initialization requirements, and a difference in size and location between the old code image and the new code image;

reconciling the incompatibilities by changing an initialization order using bootstrap code of the new code image;

copying the new code image into the memory space occupied by the old code image.

21. (Original) The method of claim 20, wherein the old code image is updated substantially concurrently with execution of regular computer operations.

22. (Original) The method of claim 20, further comprising initiating execution of a run-time segment of the new code image.

23. (Previously Presented) The method of claim 20, wherein the new code image is copied into the memory space after the incompatibilities are reconciled.

24. (Currently Amended) The method of claim 20, wherein identifying incompatibilities between the old code image and the new code image further comprises accessing capability information for the old code image and capability information for the new code image and identifying a difference between the capability information.

25. (Previously Presented) The method of claim 24, wherein reconciling the incompatibilities comprises updating modules that interface with the new code image based at least in part on a the difference between the capability information.

26. (Currently Amended) The method of claim 20, wherein reconciling the incompatibilities further comprises associating persistent data of the old code image with the new code image, such that the persistent data is available in response to execution of a run-time segment of the new code image.

27. (Canceled)

28. (Original) The method of claim 20, wherein at least one of the incompatibilities comprises a difference between data structures used by the old code image and data structures used by the new code image.

29. (Currently Amended) An apparatus for updating a code image, the apparatus comprising:

means for loading a new code image into a temporary memory location separate from a memory space occupied by and used by an old code image;

means for identifying incompatibilities between the old code image and the new code image from version information, a difference in initialization requirements, and a difference in size and location between the old code image and the new code image;

means for reconciling the incompatibilities by changing an initialization order using

bootstrap code of the new code image; and

means for copying the new code image into the memory space occupied by the old code image.

30. (Currently Amended) An article of manufacture comprising a program storage medium readable by a processor and embodying one or more instructions executable by a processor to perform a method for updating a code image, the method comprising:

loading a new code image into a temporary memory location separate from a memory space occupied by and used by an old code image;

identifying incompatibilities between the old code image and the new code image from version information, a difference in initialization requirements, and a difference in size and location between the old code image and the new code image;

reconciling the incompatibilities by changing an initialization order using bootstrap code of the new code image; and

copying the new code image into the memory space occupied by the old code image.